

N-Channel 100V MOSFET

E100N4P0OL1

V _{DS} (V)	$R_{DS(on),max}$ (m Ω)	I _D (A)
100V	4 @ V _{GS} = 10V	86

Features

- Low R_{DS(on)} trench technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

Applications

- DC/DC conversion
- Power switch
- BMS
- Moto driver

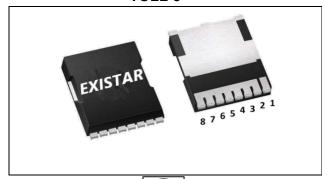
Package And Ordering Information

Ordering code	Package	Marking	
E100N4P0OL1	TOLL-8	E100N4P0OL1	

Ordering Information

Package	Units/ Reel	Reels/ Inner Box	Units/ Inner Box
TOLL-8	2000	1	2000

TOLL-8







Key Performance Parameters

Parameter	Value	Unit
VDS, min @ Tj(max)	100	V
ID, pulse	344	А
RDS(ON), max @ VGS=10V	4	mΩ
Qg	74.2	nC

Absolute Maximum Ratings at Tj=25°C Unless Otherwise Noted

Parameter	Symbol	Limit	Unit	
Drain-source voltage	V _{DS}	100		
Gate-source voltage	V_{GS}	±20	V	
T _C =25°			86	
Continuous drain current	T _C =100°C	- I _D	-	
Pulsed drain current		I _{D,pulse}	344	А
Avalanche energy, single pulse		E _{AS}	450	mJ
Davies disaination	T _C =25°C		52	
Power dissipation	T _A =25°C	P_{D}	-	W
Operating junction and storage temperature range	TJ, T _{stg}	-55 to +150	°C	

Thermal Characteristics

Parameter		Symbol	Max.	Uni t
Thermal resistance, junction-to-case	Steady state	Rejc	2.4	
Thermal resistance, junction-to-ambient	Steady state	Reja	40	°C/W

Electrical Characteristics at Tj=25°C unless otherwise specified

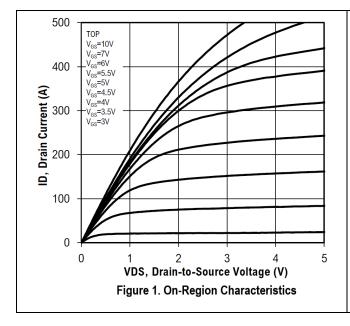
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Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions		
Static								
Drain to source breakdown voltage	V _{(BR)DSS}	100			>	V _{GS} = 0, I _D = 250 μA		
Gate-source threshold voltage	V _G s(th)	1.4	1.9	2.4	٧	V _{DS} = V _{GS} , I _D = 250 μA		
Gate-body leakage	I _{GSS}			±100	nA	V _{DS} = 0 V, V _{GS} = ±20 V		
Zero gate voltage drain current	I _{DSS}			1	μΑ	V _{DS} = 80 V, V _{GS} = 0 V		
Drain-source on-resistance	Ros(on)		3.6	4	mΩ	V _{GS} = 10 V, I _D = 20 A		
Drain-source on-resistance	Ros(on)		4.9	5.8	mΩ	V _{GS} = 4.5 V, I _D = 15 A		

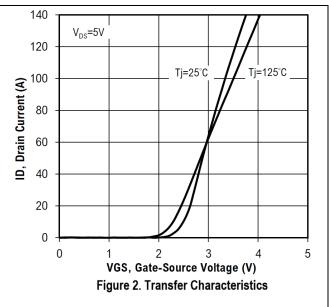
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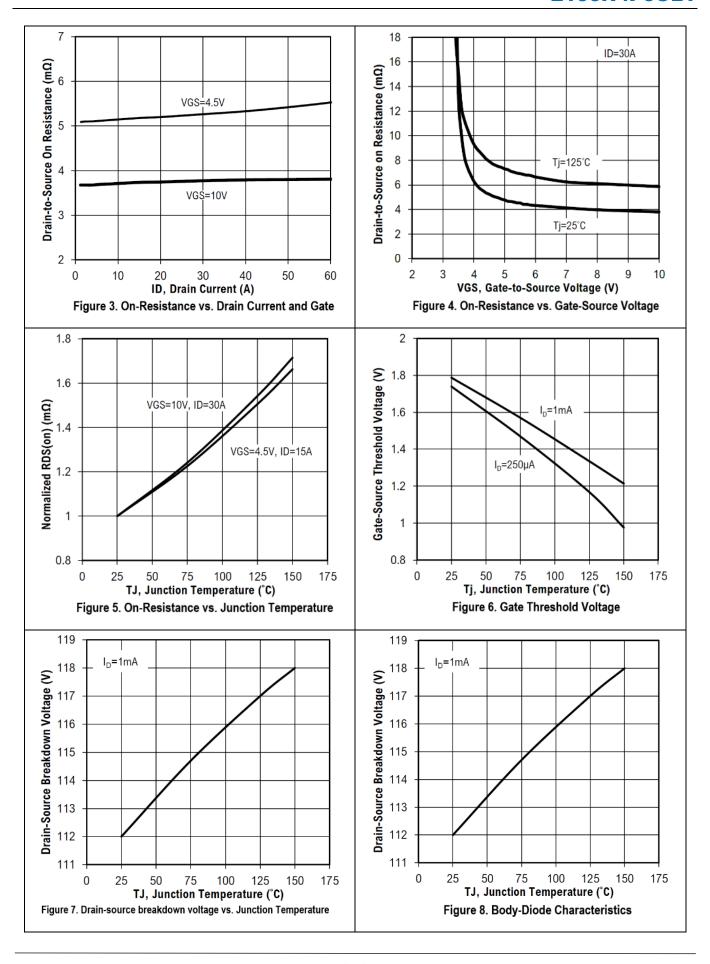
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Forward transconductance	gfs		110		S	$V_{DS} = 5 \text{ V}, I_{D} = 30 \text{ A}$	
Gate resistance	Rg		1.2		Ω	f=1MHz	
Gate Charge							
Total gate charge	Qg		74.2				
Gate-source charge	Qgs		13.5		nC	V _{DS} = 25 V, I _D = 30 A, V _{GS} = 10 V	
Gate-drain charge	Qgd		21.6				
Dynamic							
Turn-on delay time	t _{d(on)}		25.8				
Rise time	t _r		45.8			V_{DS} = 25 V, I_{D} =30 A, V_{GS} = 10 V, R_{GEN} =3.3 Ω	
Turn-off delay time	$t_{d(off)}$		23.2		ns		
Fall time	t _f		7.6				
Input capacitance	C _{iss}		4080				
Output capacitance	C _{oss}		1860		pF	V _{DS} =25 V, V _{GS} = 0 V, f = 1MHz	
Reverse transfer capacitance	C _{rss}		158				
Body Diode							
Diode forward voltage	VsD			1.2	V	V _{GS} = 0 V, I _F = 20 A	
Reverse recovery time	t _{rr}		48.7		ns	V _R = 50 V, I _S =30 A, di/dt = 100	
Reverse recovery charge	Qrr		107.6		nC	A/µs	

Electrical Characteristics Diagrams

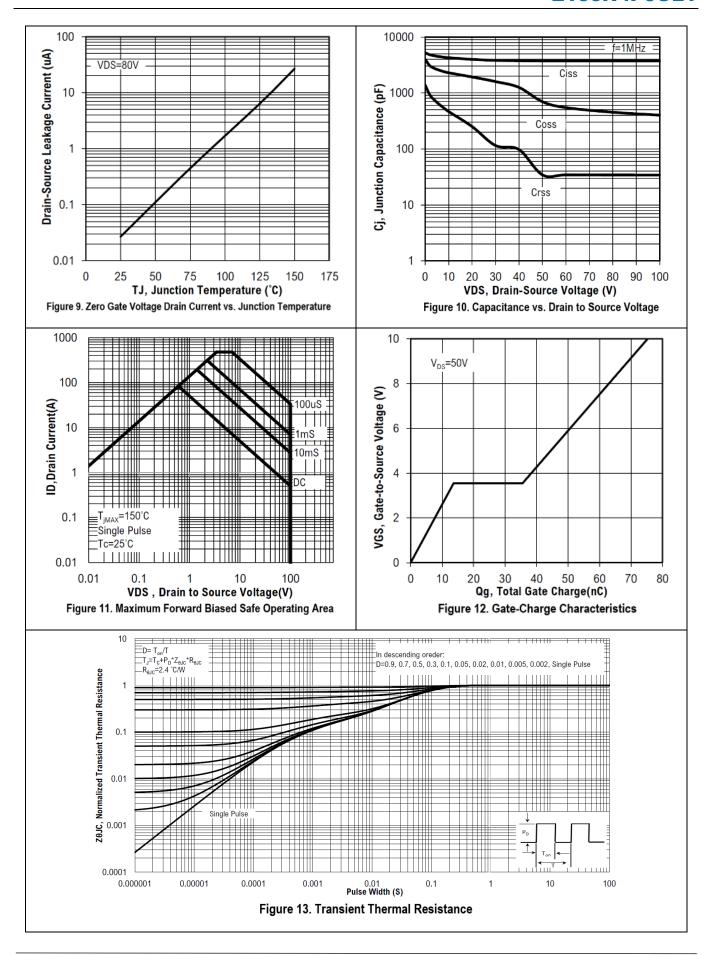




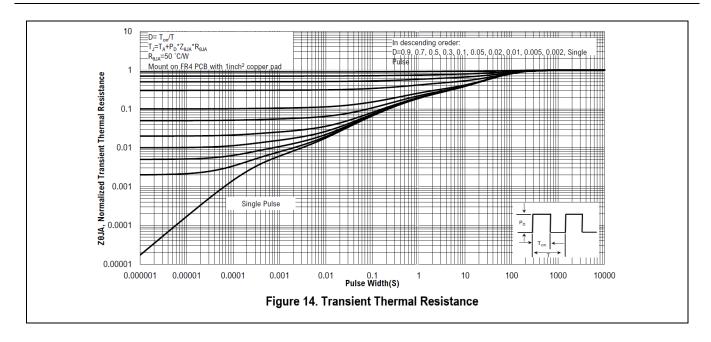








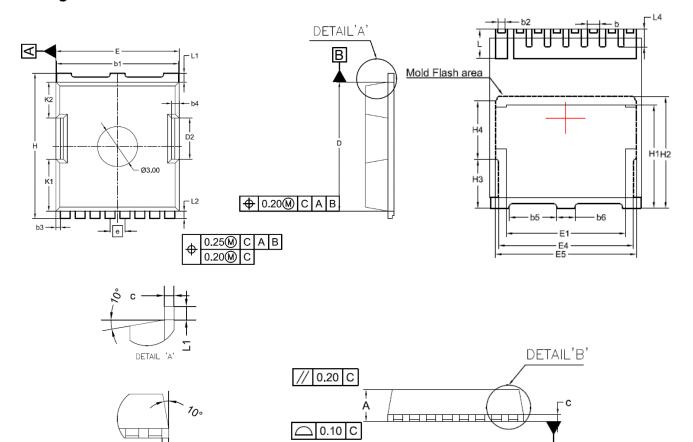






Package Outline Dimensions

DETAIL 'B'



SIDE VIEW

SYMBOLS	DIM	ENSION IN	MM	DIME	NSION IN II	NCHES	
STIVIBULS	MIN	NOM	MAX	MIN	NOM	MAX	
* A	2.200	2.300	2.400	0.087	0.091	0.094	
С	0.492	0.500	0.508	0.019	0.020	0.020	
* D	10.280	10.380	10.480	0.405	0.409	0.413	
* E	9.800	9.900	10.000	0.386	0.390	0.394	
e		1.20 BSC			0.047 BSC		
* H	11.580	11.680	11.780	0.456	0.460	0.464	
H1	6.650	6.750	6.850	0.262	0.266	0.270	
H2		7.300			0.287		
H3		3.200			0.126		
H4		3.800			0.150		
K1	4.180			0.165			
K2		2.900		0.114			
* D2		3.300		0.130			
b	0.700	0.800	0.900	0.028	0.031	0.035	
b1	9.700	9.800	9.900	0.382	0.386	0.390	
b2	0.420	0.460	0.500	0.017	0.018	0.020	
b3		0.350			0.014		
b4		0.600			0.024		
b5		3.100			0.122		
b6		1.200		0.047			
L	1.700	1.900	2.100	0.067	0.075	0.083	
L1		0.700		0.028			
L2		0.600		0.024			
L4	1.050	1.150	1.250	0.041	0.045	0.049	
L5	0.500	0.600	0.700	0.020	0.024	0.028	
E1		7.800		0.31			
E4		8.800		0.35			
E5		9.200			0.36		



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