

## N-Channel 60V MOSFET

### E060N5P4HL1

$V_{DS}$ (V)	$R_{DS(on),max}$ (m $\Omega$ )	$I_D$ (A)
60V	5.4@ $V_{GS} = 10V$	110

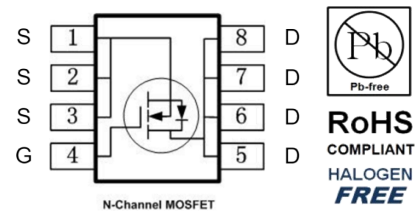
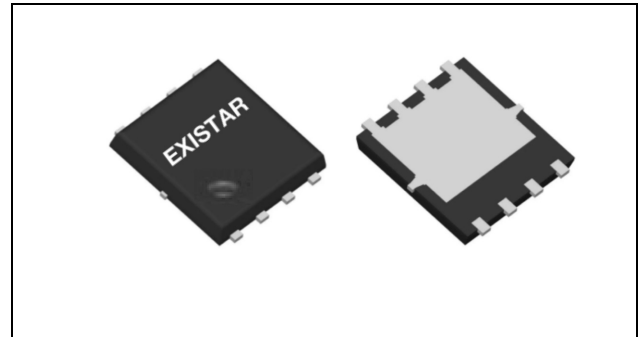
### Features

- Low  $R_{DS(on)}$  trench technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

### Applications

- DC/DC conversion
- Power switch
- PD charger
- Moto driver

### PDFN5X6



### Package And Ordering Information

Ordering code	Package	Marking
E060N5P4HL1	PDFN5X6	E060N5P4HL1

### Ordering Information

Package	Units/ Reel	Reels/ Inner Box	Units/ Inner Box
PDFN5X6	5000	1	5000

**Key Performance Parameters**

Parameter	Value	Unit
V <sub>DS</sub> , min @ T <sub>j</sub> (max)	60	V
I <sub>D</sub> , pulse	320	A
R <sub>DS(ON)</sub> , max @ V <sub>GS</sub> =10V	5.4	mΩ
Q <sub>g</sub>	30	nC

**Absolute Maximum Ratings at T<sub>j</sub>=25°C Unless Otherwise Noted**

Parameter		Symbol	Limit	Unit
Drain-source voltage		V <sub>DS</sub>	60	V
Gate-source voltage		V <sub>GS</sub>	±20	
Continuous drain current	T <sub>C</sub> =25°C	I <sub>D</sub>	110	A
	T <sub>C</sub> =100°C		-	
Pulsed drain current		I <sub>D,pulse</sub>	320	
Avalanche energy, single pulse		E <sub>AS</sub>	66	mJ
Power dissipation	T <sub>C</sub> =25°C	P <sub>D</sub>	87	W
	T <sub>A</sub> =25°C		-	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C

**Thermal Characteristics**

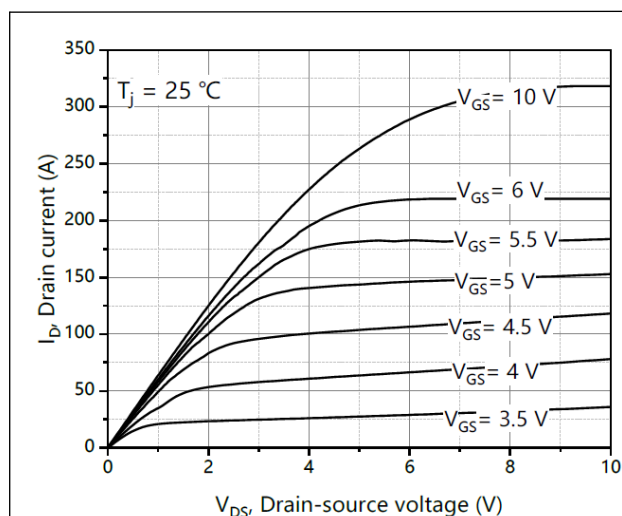
Parameter		Symbol	Max.	Unit
Thermal resistance, junction-to-case	Steady state	R <sub>θJC</sub>	1.44	°C/W
Thermal resistance, junction-to-ambient	Steady state	R <sub>θJA</sub>	62	

**Electrical Characteristics at T<sub>j</sub>=25°C unless otherwise specified**

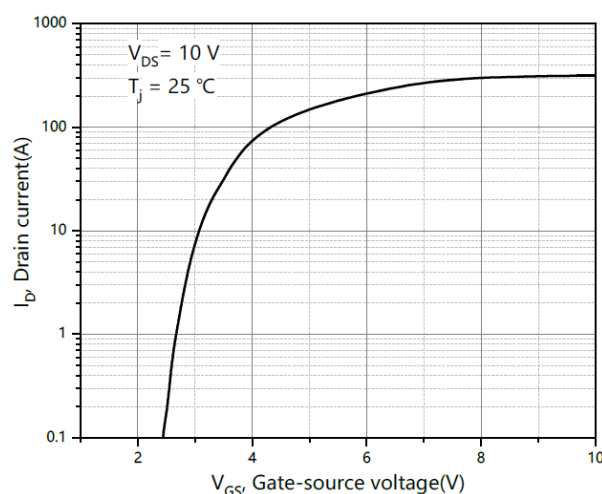
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test conditions
<b>Static</b>						
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	60			V	V <sub>GS</sub> = 0, I <sub>D</sub> = 250 μA
Gate-source threshold voltage	V <sub>GS(th)</sub>	1.0		2.5	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA
Gate-body leakage	I <sub>GSS</sub>			±100	nA	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±20 V
Zero gate voltage drain current	I <sub>DSS</sub>			1	μA	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V
Drain-source on-resistance	R <sub>DS(on)</sub>		3.8	5.4	mΩ	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A
Drain-source on-resistance	R <sub>DS(on)</sub>		5.0	6.2	mΩ	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 10 A

Forward transconductance	gfs		-		S	VDS = 5 V, ID = 20 A
Gate resistance	Rg		2.8		Ω	f=1MHz
Gate Charge						
Total gate charge	Qg		30		nC	VDS = 50 V, ID = 25 A, VGS = 10 V
Gate-source charge	Qgs		5.8			
Gate-drain charge	Qgd		6.1			
Dynamic						
Turn-on delay time	td(on)		22.9		ns	VDS = 50 V, ID =25 A, VGS = 10 V, RGEN = 2 Ω
Rise time	tr		6.5			
Turn-off delay time	td(off)		45.7			
Fall time	tf		20.4			
Input capacitance	Ciss		1750		pF	VDS =50 V, VGS = 0 V, f = 100kHz
Output capacitance	Coss		332			
Reverse transfer capacitance	Crss		10.6			
Body Diode						
Diode forward voltage	VSD			1.3	V	VGS = 0 V, IF = 20 A
Reverse recovery time	trr		50.3		ns	VR= 50 V, IS =25 A, di/dt = 100 A/μs
Reverse recovery charge	Qrr		45.1		nC	

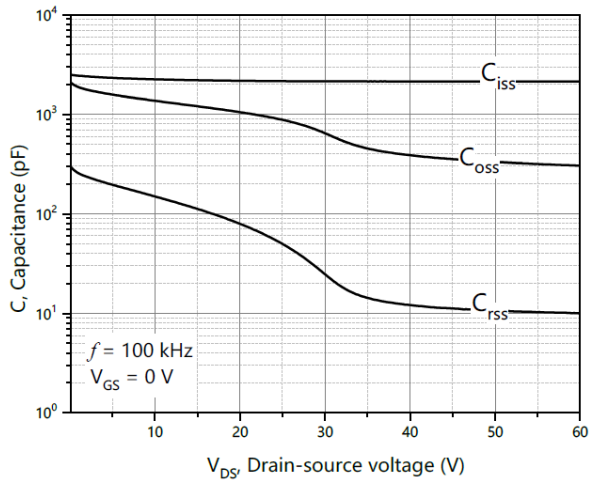
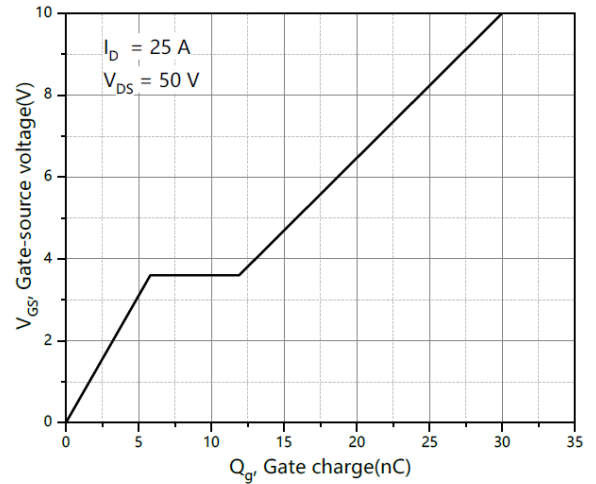
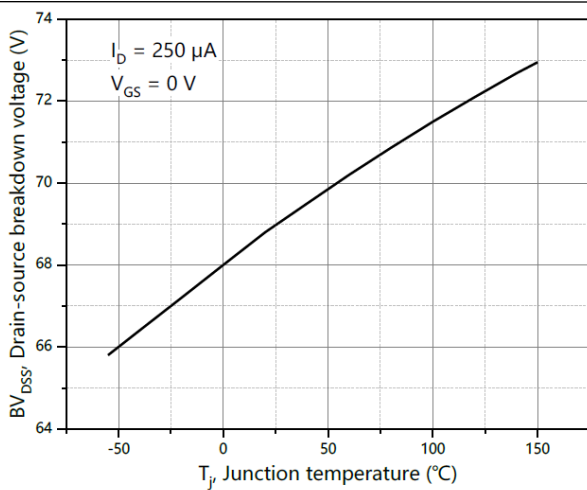
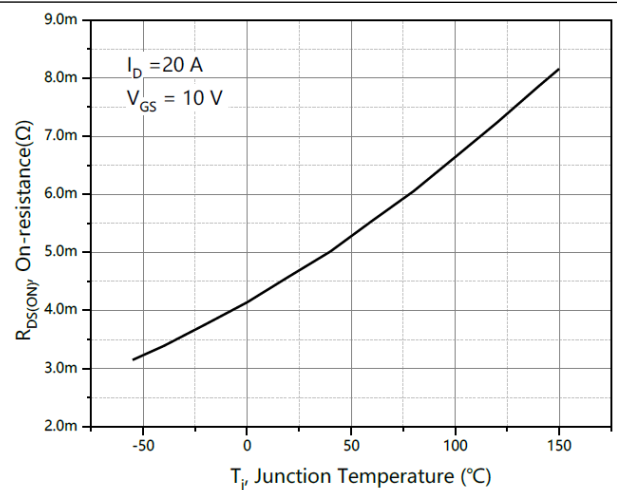
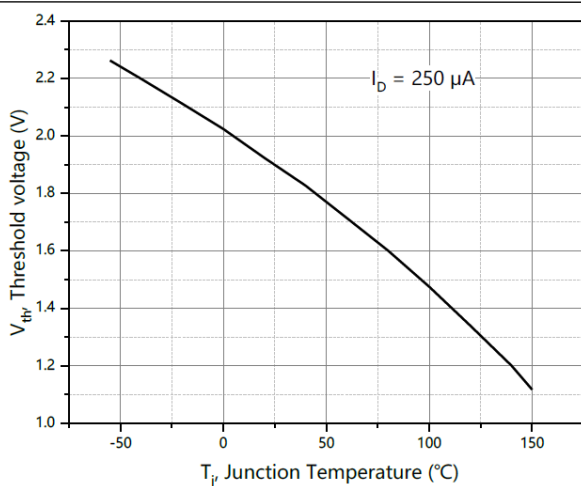
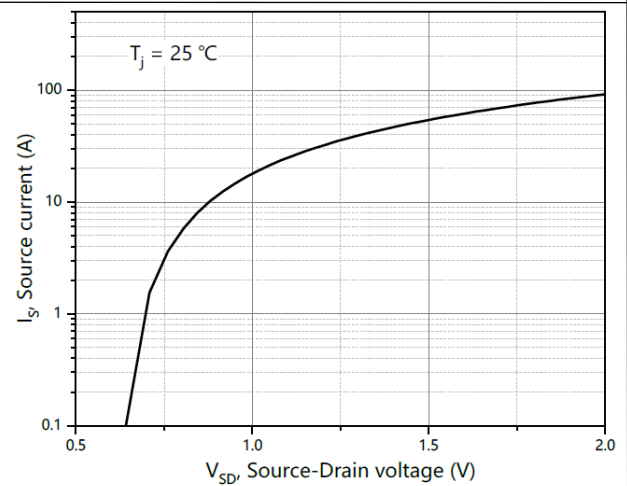
## Electrical Characteristics Diagrams

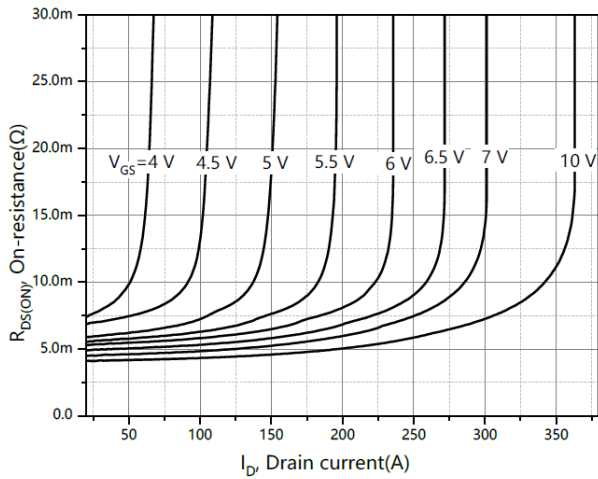
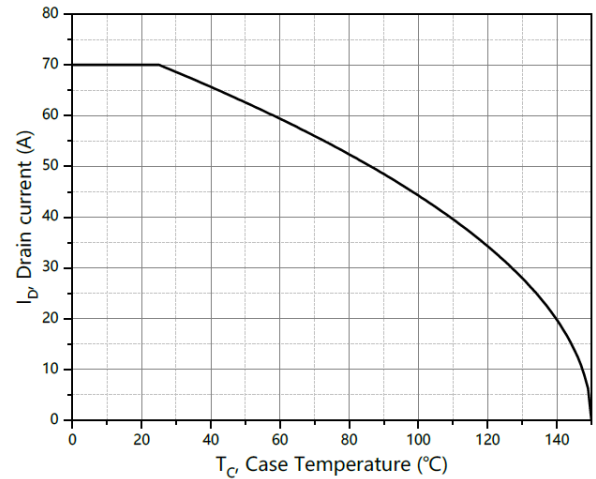
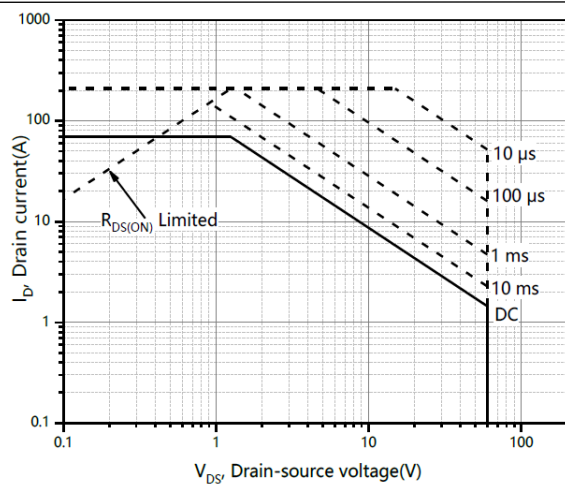
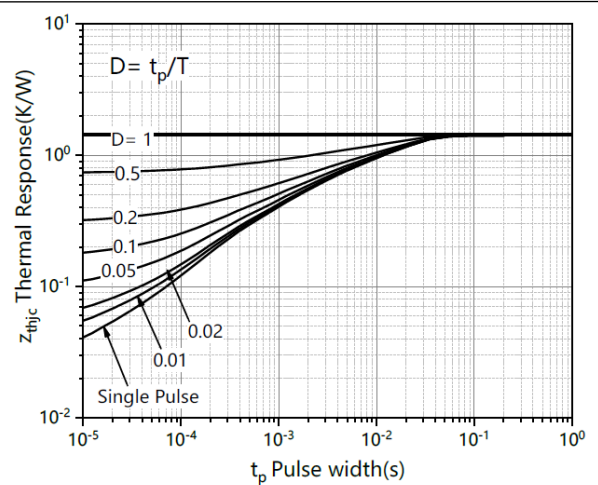


**Figure 1. Typ. output characteristics**



**Figure 2. Typ. transfer characteristics**


**Figure 3. Typ. capacitances**

**Figure 4. Typ. gate charge**

**Figure 5. Drain-source breakdown voltage**

**Figure 6. Drain-source on-state resistance**

**Figure 7. Threshold voltage**

**Figure 8. Forward characteristic of body diode**


**Figure 9. Drain-source on-state resistance**

**Figure 10. Drain current**

**Figure 11. Safe operation area  $T_C = 25^{\circ}\text{C}$** 

**Figure 12. Max. transient thermal impedance**

## Test circuits and waveforms

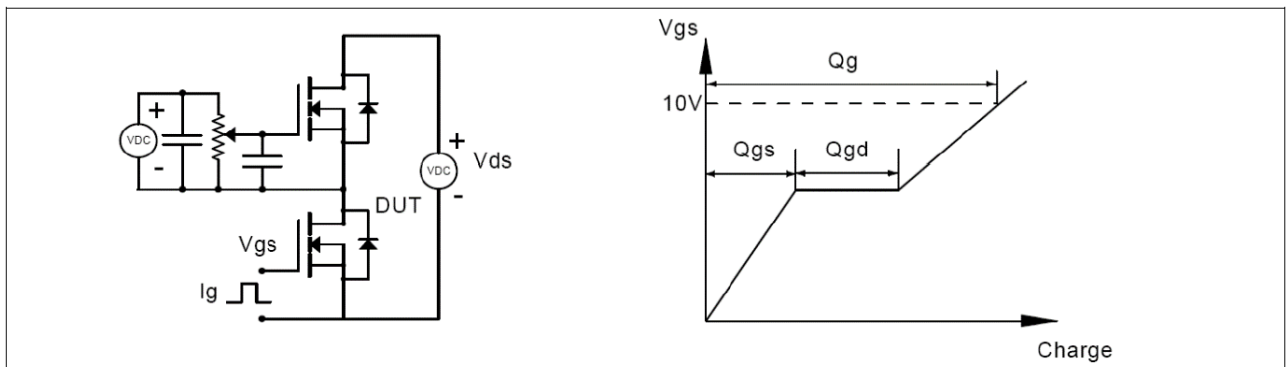


Figure 1. Gate charge test circuit & waveform

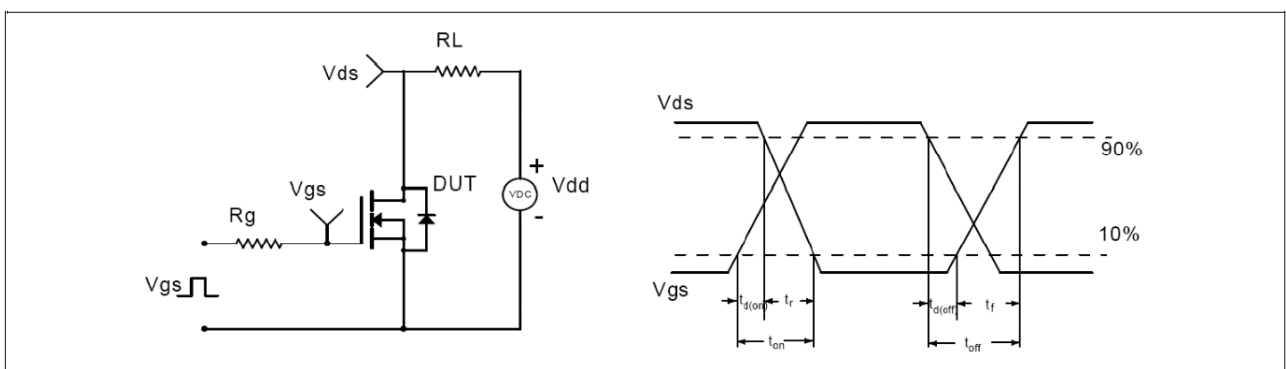


Figure 2. Switching time test circuit & waveforms

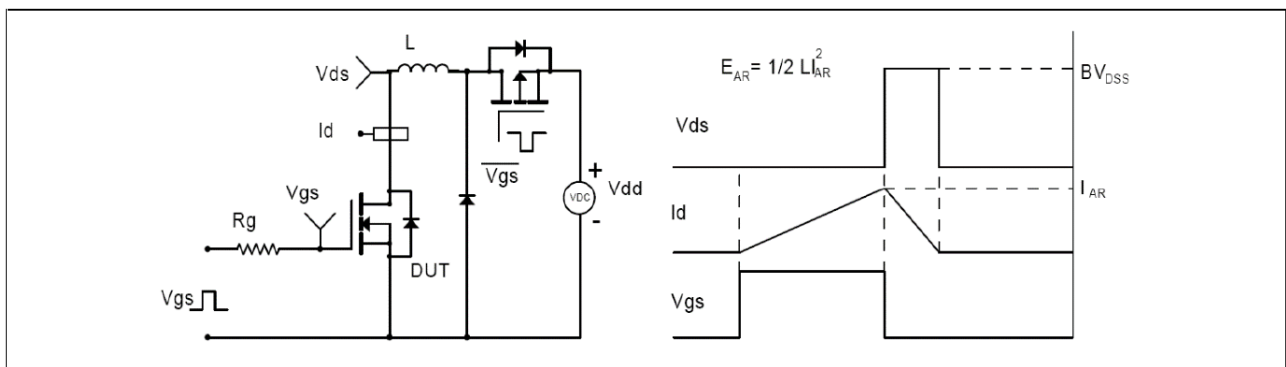


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

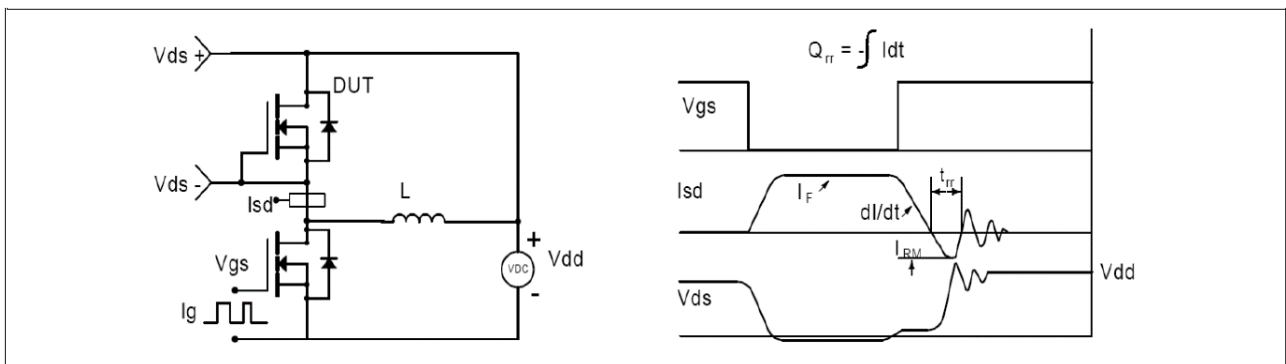
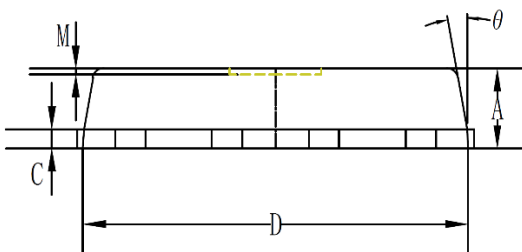
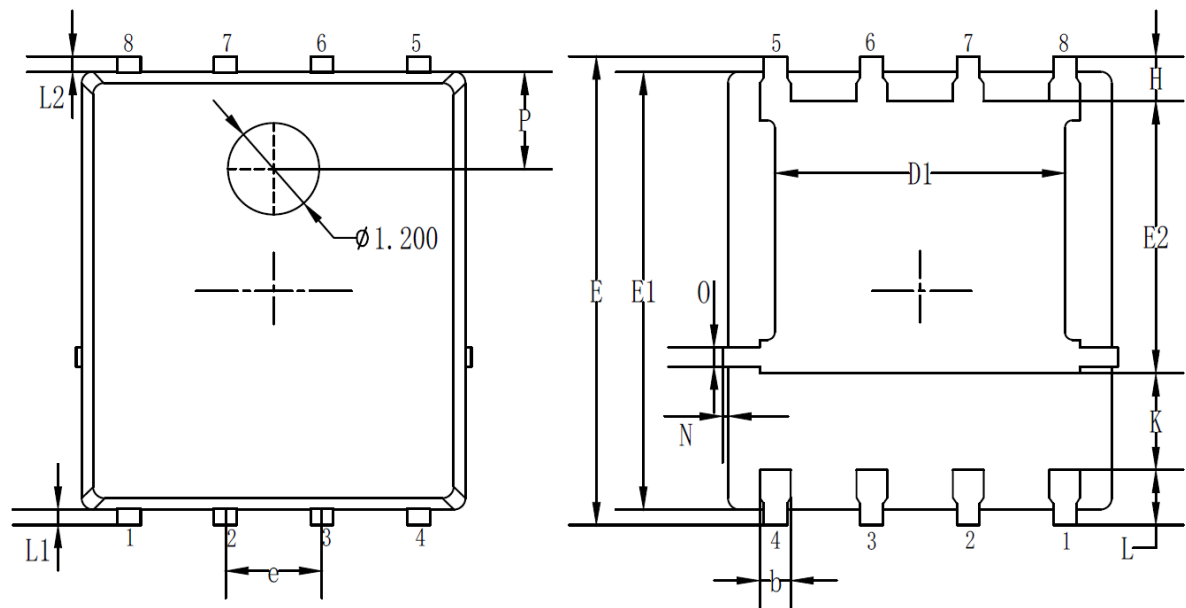


Figure 4. Diode reverse recovery test circuit & waveforms

**Package Outline Dimensions**


Symbols	Millimeters		
	MIN.	NOM.	MAX.
A	0.90	1.05	1.20
b	0.35	0.40	0.50
C	0.20	0.25	0.35
D	4.90	5.05	5.20
D1	3.72	3.82	3.92
E	0.60	6.15	6.30
E1	5.60	5.75	5.90
E2	3.47	3.57	3.67
e	1.27 BSC.		
H	0.48	0.58	0.68
K	1.17	1.27	1.37
L	0.64	0.74	0.84
L1/L2	0.20 REF.		
θ	8°	10°	12°
M	0.08 REF.		
N	0	-	0.15
O	0.25 REF.		
P	1.28 REF.		

## **Legal Disclaimer**

The information given in this document shall be for illustrative purposes only and shall in no event be regarded as a guarantee of conditions or characteristics. Existar Technologies reserves the right to change any information herein. With respect to any examples, hints or any typical values stated herein and/or any information regarding the application of the product, Existar Technologies or its affiliates hereby make no representation or warranty of any kind, expressed or implied, as to any information provided hereunder, including without limitation as to the accuracy, completeness or non-infringement of intellectual property rights of any third party, and they assume no liability for the consequences of use of such information. In addition, any information given in this document is subject to customer's compliance with its obligations stated herein and any applicable legal requirements, norms and standards concerning customer's products and any use of the product of Existar Technologies in customer's applications. The information contained herein is exclusively intended for technically trained staff. No license is granted by implication under any patent right, copyright, mask work right, or other intellectual property right. It is customer's sole responsibility to evaluate the suitability of the product for the intended application and the completeness of the product information given herein with respect to such application. In no event shall Existar Technologies or its affiliates be liable to any party for any direct, indirect, special, punitive, incidental or consequential damages of any nature whatsoever, including but not limited to loss of profits and loss of goodwill, whether or not such damages are based on tort or negligence, warranty, breach of contract or any other legal theory.